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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/736,636

12/17/2003

Chi-Kuang Lai

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22852

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12/16/2005

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EXAMINER

VU, PHU

ART UNIT

PAPER NUMBER

2871

DATE MAILED: 12/16/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/736,636

Applicant(s)

LAI, CHI-KUANG

Examiner

Phu Vu

Art Unit

2871

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 17.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) 8-16 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-8 and 17-20 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 17 December 2003 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-7 and 17-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Ide 6707515.

Regarding claims 1-3, Ide teaches a liquid crystal display comprising: a backlight source (26), an EOLS (18) including a plurality of regions arranged, and an LCD panel (16) disposed to sandwich the EOLS with the backlight source receiving the light passing through the EOLS to display the image. The limitation of EOLS is interpreted broadly as it appears from applicant's specification an EOL shares the structure with liquid crystal display that is driven to act like a shutter. The driving mechanics do not appear to be any difference, thus limitation of during a time frame controlling the light from the backlight source to pass the regions in display time, the backlight source to pass each of the regions in sequence or the same time is are product by process limitation and Ide teaches any structure implied by this limitation.

Regarding claim 4, Ide teaches the EOLS comprising a first substrate, a first electrode layer on the first substrate comprising a plurality of transparent electrodes formed in parallel to one another (see fig. 3 element 8) and a second electrode layer on

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the second substrate (fig. 3 element 7), and an LC layer (fig. 3 element 3) between the first and second substrate.

Regarding claim 5, Ide teaches the electrodes layers comprising ITO (column 4 lines 55-60).

Regarding claim 6, Ide teaches the LC layer comprising ferroelectric LC (column 3 line 20).

Regarding claim 7, the reference teaches the EOLS further comprising a polarizer on which the first substrate is disposed (fig. 18 element 14).

Regarding claim 17, Ide teaches a liquid crystal display comprising: providing a backlight source (26), emitting light from the backlight; providing an EOLS (18) including a first electrode layer on the first substrate comprising a plurality of transparent electrodes formed in parallel to one another (see fig. 3 element 8) and a second electrode layer on the second substrate (fig. 3 element 7), and an LC layer (fig. 3 element 3) between the first and second substrate. The limitation of selectively biasing the transparent electrodes to selectively allow the light from the backlight source to pass through the EOLs is met as the limitation does not elaborate on what selectively is meant. Therefore, any arbitrary biasing that occurs in the reference can be referred to as select biasing.

Regarding claim 18, the limitation of sequentially biasing is extremely broad. In any instance where a first electrode is biased prior to a second electrode sequential biasing occurs according to a broadest reasonable interpretation, which will occur in any LCD display.

Regarding claim 19, the limitation of biasing the transparent electrodes at the same time is broadly interpreted as biasing any two electrodes at the time, which will occur in any LCD display.

Regarding claim 20, the limitation of the first and second layers being formed of ITO the reference teaches this limitation (see column 4 lines 55-60).

Claims 1, 4 and 8 are rejected under 35 U.S.C. 102(b) as being anticipated by Chang U20030227585.

Regarding claims 1, 4 and 8, Chang teaches a liquid crystal display comprising: a backlight source (fig. 1 element 26), an EOLS (14) including a plurality of regions arranged, and an LCD panel (12) disposed to sandwich the EOLS with the backlight source receiving the light passing through the EOLS to display the image. The limitation of EOLS is interpreted broadly as it appears from applicant's specification an EOL shares the structure with liquid crystal display that is driven to act like a shutter. The driving mechanics do not appear to be any difference, thus limitation of during a time frame controlling the light from the backlight source to pass the regions in display time, the backlight source to pass each of the regions in sequence or the same time is are product by process limitation and change teaches any structure implied by this limitation.

Regarding claim 4, Chang teaches the EOLS comprising a first substrate, a first electrode layer on the first substrate comprising a plurality of transparent electrodes formed in parallel to one another and a second electrode layer on the second substrate and an LC layer between the first and second substrate (see fig. 1).

Regarding claim 8, Chang teaches the EOLS further comprising a first polarizer on which the first substrate is disposed (26) and a second polarizer on which the second substrate is disposed (24).

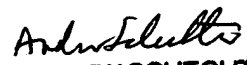
Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phu Vu whose telephone number is (571)-272-1562. The examiner can normally be reached on 8AM-5PM M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim can be reached on (571)-272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Phu Vu
Examiner
AU 2871


ANDREW SCHECHTER
PRIMARY EXAMINER